

Université de RENNES 1 – ENSSAT	
Année Universitaire 2021-2022	revision of 04/10/21
Spring Semester	2nd year Digital Systems Engineering for International Students

Teaching Unit	Module	ECTS	Assessment		Work Load					
			Session1	Session2	Lectures	Tutorial Classes	Practical work	personal working hours		
Humanities	French as a foreign language	2	Continuous	none		30			20	
	Physical Education	2	Continuous	none		30			20	
	English	2	Continuous	none		30			20	
Technical	Digital communications	2	Continuous	none	12	4	10		17	Communications numériq
	Image processing	2	Continuous	none	12	6	8		17	Traitement des Images
	Electronic interface	2	Continuous	Examination	6	8	16		20	Interfacage
	VHDL based design	4	Practical Work & Project	Examination	8	10	40		39	Synthèse VHDL
	VLSI Integrated circuits and systems	2	Continuous	Examination	18	4	4		17	Conception de Circuits Int
	Real-time systems	3	Continuous & Project	Examination	12	12	12		24	Systèmes temps-réel
	Advanced algorithmic	2			14	12			17	Algorithmique avancée
Projects	Integration Project	7	Continuous	none			50		100	